

EAST - [10623490.wsp:1]

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Drafts
 BRS: (stack\$2 near semiconductor) and first adj substrate
 Pending
 Active
 Failed
 Saved
 (2) ("6621169").PN
 (2698) stack\$2 near semiconductor
 (100) (stack\$2 near semiconductor) and first adj substrate
 Favorites
 Tagged (0)
 UDC
 Queue
 Trash

Browse Queue Clear

DBs: USPAT, EPO, JPO, DERWENT, IBM, TDB

Default operator: OR

Plurals

Highlight all hit terms initially

(stack\$2 near semiconductor) and first adj substrate

BRST... SAR... Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6683012 B2	20040127	7	Method for epitaxially growing crystalline insulation layer on crystalline silicon	438/785	257/410; 257/411;
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6677214 B1	20040113	301	Semiconductor device and method of fabricating the same	438/365	438/378; 438/486;
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6674948 B2	20040106	24	Optoelectronic IC module	385/120	385/88; 438/22
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6670702 B2	20031230	16	Stackable ball grid array package	257/686	257/685; 257/724
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6670599 B2	20031230	39	Semitransparent optical detector on a flexible substrate and method of making	250/214.1	257/80; 438/141
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6653677 B2	20031125	13	Semiconductor constructions	257/300	
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6638834 B2	20031028	13	Methods of forming semiconductor constructions	438/455	
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6636280 B1	20031021	29	Liquid crystal display device having improved TFTs and a fabrication method	349/43	257/72; 257/E21.413;
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6635552 B1	20031021	13	Methods of forming semiconductor constructions	438/455	438/482; 438/486
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6621169 B2	20030916	37	Stacked semiconductor device and method of producing the same	257/780	257/48; 257/697;
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6611052 B2	20030826	19	Wafer level stackable semiconductor package	257/686	257/698